

SPECIFICATION SHEET NO.	S1107 – LGE3M40065BL0T	
ORIGINAL MFG/PART NO.	 LGE Diodes/LGE3M40065B -L	
NEXTGEN PART CODE	LGE3M40065BL0T	Indicate This Code For RFQ /Order
DATE	Nov. 07, 2025	
REVISION	A3	Updated With Most Recent Data
DESCRIPTION AND MAIN PARAMETRICS	<p>Silicon Carbide (SiC) MOSFET, 3 Pins, Case TO-247-3, LGE3M L Series, N-Channel, Drain-Source Voltage (V_{DS}): 650V</p> <p>Drain-Source On-State Resistance R_{DS(ON)}: 40mΩ</p> <p>Continuous Drain Current (I_D) @ T_c=25°C: 72A</p> <p>Operating Temperature: -55°C ~ 175°C (T_J)</p> <p>Package in Tube, 30pcs/Tube</p> <p>RoHS/RoHS III compliant, RoHS Annex III lead Exemption (Exempt per RoHS EU 2015/863) and Halogen Free (HF)</p>	
CUSTOMER		
CUSTOMER PART NUMBER		
CROSS REF. PART NUMBER		
MEMO		

VENDOR APPROVE		
Issued/Checked/Approved		
		
Effective Date: Nov. 07, 2025		

CUSTOMER APPROVE	
Date:	

MAIN FEATURE

- High Blocking Voltage with Low On-Resistance
- High Speed With Low Capacitance
- High Operating Junction Temperature Capability
- Very Fast And Robust Intrinsic Body Diode
- Higher System Efficiency
- Parallel Device Convenience Without Thermal Runaway
- High Temperature Application
- Hard Switching & Higher Reliability
- Easy to drive
- Meet MSL 1 Requirement
- Cross Competitors Parts and More.
- RoHS/RoHS III compliant, RoHS Annex III lead Exemption (Exempt per RoHS EU 2015/863) and Halogen Free (HF)



Image shown is a representation only. Exact specifications should be obtained from the product dimension.



APPLICATION

- Electric Vehicle Charging Device
- Server And Communication Power
- Photovoltaic Inverter
- UPS Power Supply
- High Voltage DC / DC Converter
- Switching Mode Power Supplies

ELECTRICAL CHARACTERISTICS

- See Page 5 ~ Page 7.
- All Products Parameters are Subject To NextGen Components' Final Confirmation.

HOW TO ORDER

- Please Follow Up Part Code Guide And Indicate NextGen Part Code LGE3M40065BL0T For RFQ and Order.

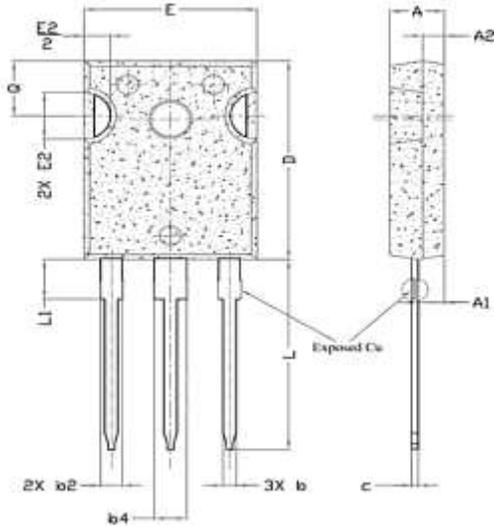
PART CODE GUIDE

RFQ
Request For Quotation

CODE	NAME	KEY SPECIFICATION OPTION
LGE3M	Product Series Code	Silicon Carbide (SiC) MOSFET, 3 Pins, Case TO-247-3, LGE3M L Series
40	Drain-Source On-State (V _{DS}) Resistance R _{DS(ON)} Code	40: 40mΩ
065	Drain-Source Voltage (V _{DS}) Code	065: 650V Max.
B	Package Case Code	B: TO-247-3; E: TO-263-2; J: TO-263-7; Q: TO-247-4;
LOT	Internal Control Code	Letter A~Z, a-z or Digits (0-9)
XX	Special/Custom Parameters	Blank: N/A; XX: Letter A~Z, a~z or digits (0~9) for Special/Custom Parameters

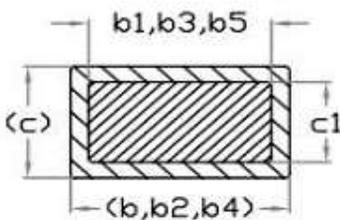
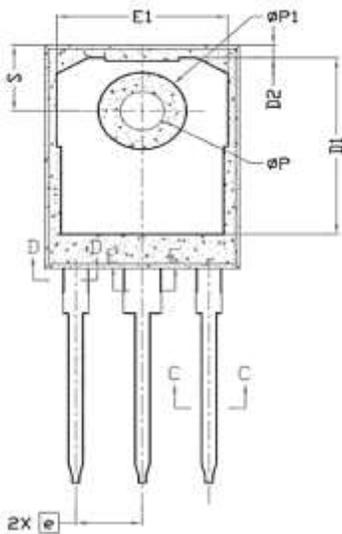
DIMENSION -- Unit: (mm), Case TO-247-3 Outline

Top View



SYMBOL	TO-247-3			NOTES
	Min.	Typ.	Max.	
A	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.50	2.00	2.49	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	6,8
b5	2.87	3.00	3.18	
c	0.55	0.60	0.69	6
c1	0.55	0.60	0.65	
D	20.8	20.95	21.1	4
D1	16.25	16.55	17.65	5
D2	0.51	1.19	1.35	
E	15.75	15.94	16.13	4
E1	13.46	14.02	14.16	5
E2	4.32	4.91	5.49	3
e	5.44BSC			
L	19.81	20.07	20.32	6
L1	4.10	4.19	4.40	7
ØP	3.56	3.61	3.65	
ØP1	7.19REF.			
Q	5.39	5.79	6.20	
S	6.04	6.17	6.30	

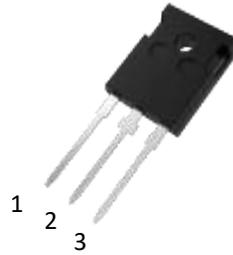
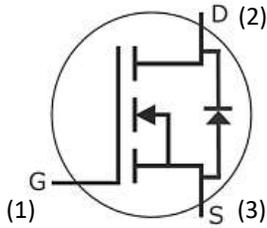
Side View



Section C-C,D-D,E-E

Note:

1. Package Reference: JEDEC TO247, Variation AD.
2. Slot Required, Notch May Be Rounded
3. Dimension D & E Do Not Include Mold Flash.

INTERNAL CIRCUIT DIAGRAM

650V N-CHANNEL SiC MOSFET

V _{DS}	I _D @ T _c =25°C	R _{DS(on)}	MARKING	PACKAGE/CASE
650V	72A	40mΩ	LGE3M40065B	TO-247-3

MAX. RATINGS @T_c=25 °C (Unless Otherwise Specified)

PARAMETER	SYMBOL	TEST CONDITIONS	VALUE	UNIT
Drain-Source Voltage	V _{DS}	V _{GS} = 0V, I _D = 100μA	650	V
Gate-Source Voltage	V _{GS}	Recommended maximum	-5/+20	V
Continuous Drain Current	I _D	V _{GS} = 20V, T _c = 25°C	72	A
		V _{GS} = 20V, T _c = 100°C	58	
Pulsed Drain Current	I _{DM}	Pulse width limited by SOA	180	A
Power Dissipation	P _{TOT}	T _c = 25°C	348	W
Operating Junction Temperature Range	T _J		-55 ~ +175	°C
Storage Temperature Range	T _{STG}		-55 ~ +175	°C
Solder Temperature	T _L	Wave soldering only allowed at leads, 1.6 mm from case for 10 s	260	°C

ELECTRICAL CHARACTERISTICS PART I - @Tc=25 °C (Unless Otherwise Specified)

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			Min.	Typ.	Max.	
Zero Gates Voltage Drain Crurent	I _{DSS}	V _{DS} =650V, V _{GS} =0V	-	3	100	μA
Gate Leakage Crurent	I _{GSS}	V _{GS} =-5/20V, V _{DS} =0V	-	1	±100	nA
Gate Threshold Voltage	V _{TH}	V _{DS} =V _{GS} , I _D =6.1mA,	-	3.2	-	V
		V _{DS} =V _{GS} , I _D =6.1mA, T _c =175°C	-	2.2	-	
Drain-Source On-State Resistance	R _{ON}	V _{GS} =20V, I _D =20A, T _J =25°C	-	40	55	mΩ
		V _{GS} =20V, I _D =20A, T _J =175°C	-	53	-	
Input Capacitance	C _{iss}	V _{DS} =600V, V _{GS} =0V, f=1MHz, V _{AC} =25mV	-	2692	-	pF
Output Capacitance	C _{oss}		-	179	-	pF
Reverse Transfer Capacitance	C _{rss}		-	10.8	-	pF
The output capacitor stores energy	E _{oss}		-	35.6	-	μJ
Total Gate Charge	Q _g	V _{DS} =400V, I _D =20A, V _{GS} =-5V/20V	-	110.8	-	nC
Gate To Source Charge	Q _{gs}		-	26.8	-	nC
Gate To Drain Charge	Q _{gd}		-	35.7	-	nC
Gate Input Resistance	R _g	f=1MHz	-	2	-	Ω
Turn-On Switching Energy	E _{on}	V _{DS} =400V, V _{GS} =-2V/20V, I _D =30A, R _{G(ext)} =3.3Ω, L=450μH	-	289.1	-	μJ
Turn-Off Switching Energy	E _{off}		-	117.1	-	μJ
Turn-On Delay Time	t _{d(on)}		-	24.7	-	ns
Rise Time	t _r		-	20.3	-	ns
Turn-Off Delay Time	t _{d(off)}		-	12.4	-	ns
Fall Time	t _f		-	29.6	-	ns

ELECTRICAL CHARACTERISTICS PART II - @T_c=25 °C (Unless Otherwise Specified)

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			Min.	Typ.	Max.	
Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _{SD} = 20A,	-	4.0	-	V
		V _{GS} = 0V, I _{SD} = 20A, T _J = 175°C	-	3.6	-	
Reverse Recovery Time	t _{rr}	V _{GS} = -2V/+20V, I _{SD} = 30A	-	23	-	ns
Reverse Recovery Charge	Q _{rr}	V _R = 400V, di/dt = 1700 A/μs,	-	161	-	nC
Reverse Recovery Peak Current	I _{rrm}	R _{G(ext)} = 3.3Ω L = 450μH	-	10.4	-	A

THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			MIN.	TYP.	MAX.	
Thermal Resistance To Shell	R _{th(j-c)}		-	0.431	-	°C/W

TYPICAL PERFORMANCE (For Reference Only)

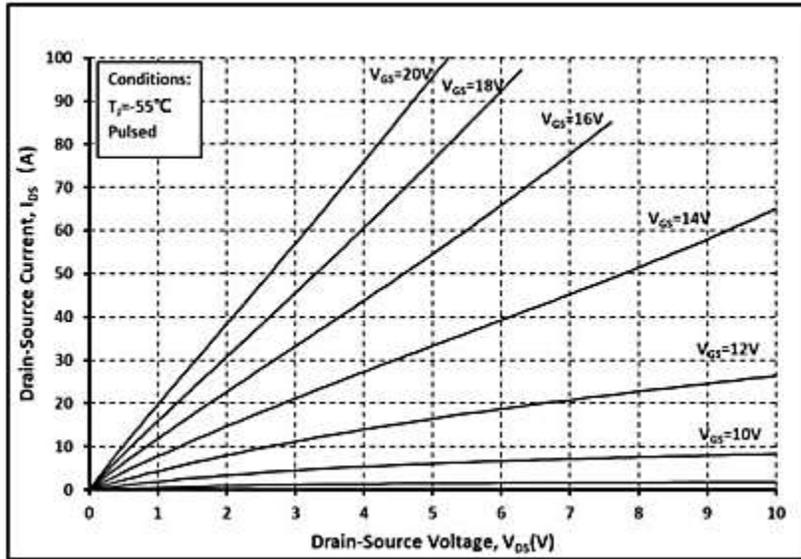


Figure 1. Output Characteristics $T_j = 55^\circ\text{C}$

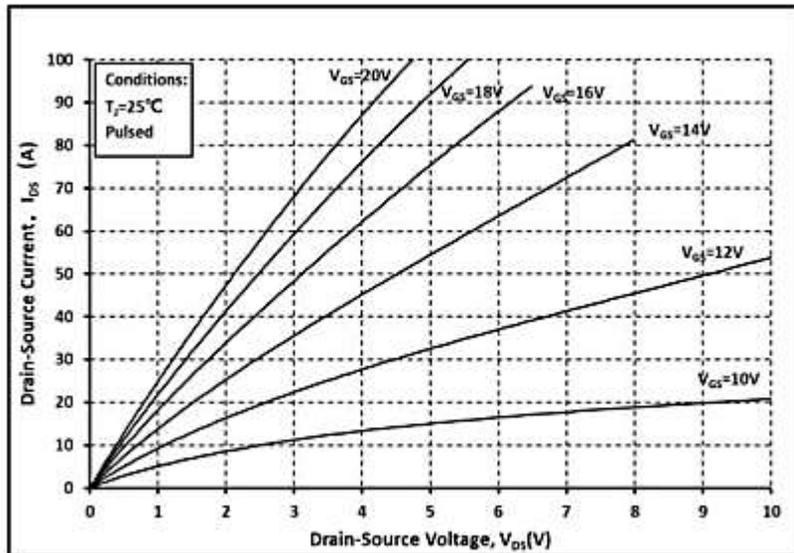


Figure 2. Output Characteristics $T_j = 25^\circ\text{C}$

TYPICAL PERFORMANCE (For Reference Only)

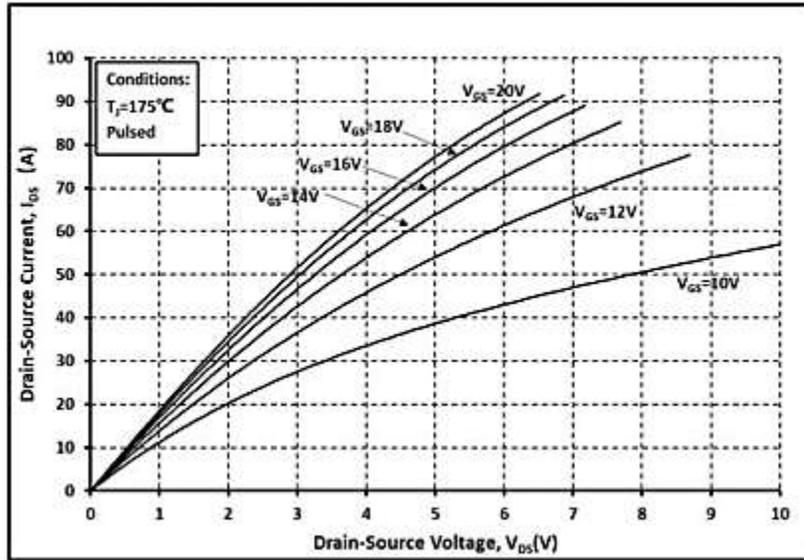


Figure 3. Output Characteristics $T_J=175^\circ\text{C}$

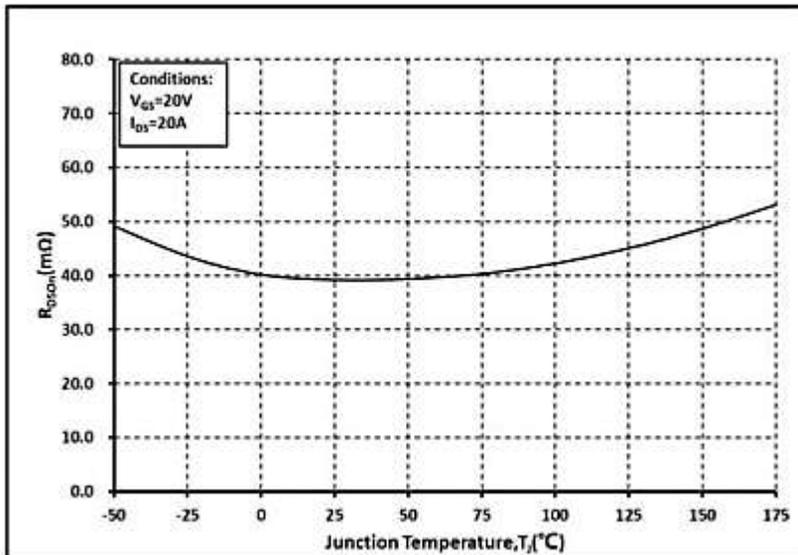


Figure 4. On-Resistance vs. Temperature

TYPICAL PERFORMANCE (For Reference Only)

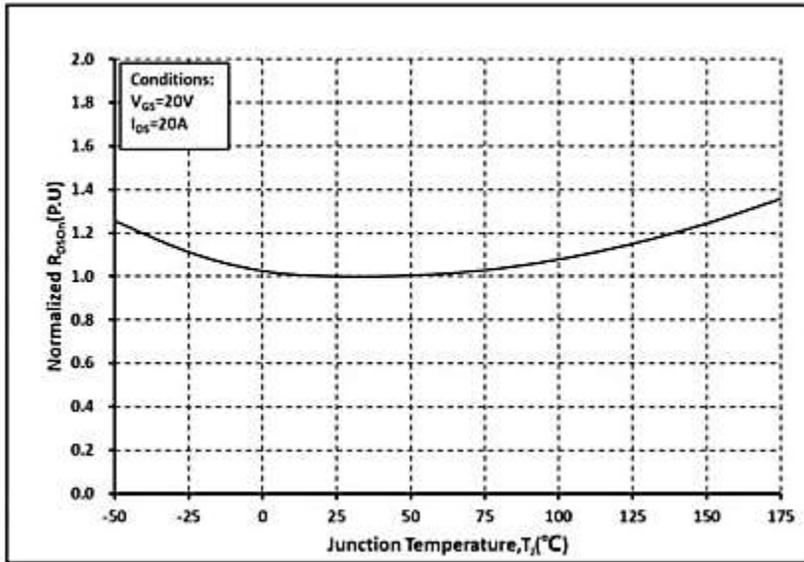


Figure 5. Normalized On-Resistance vs. Temperature

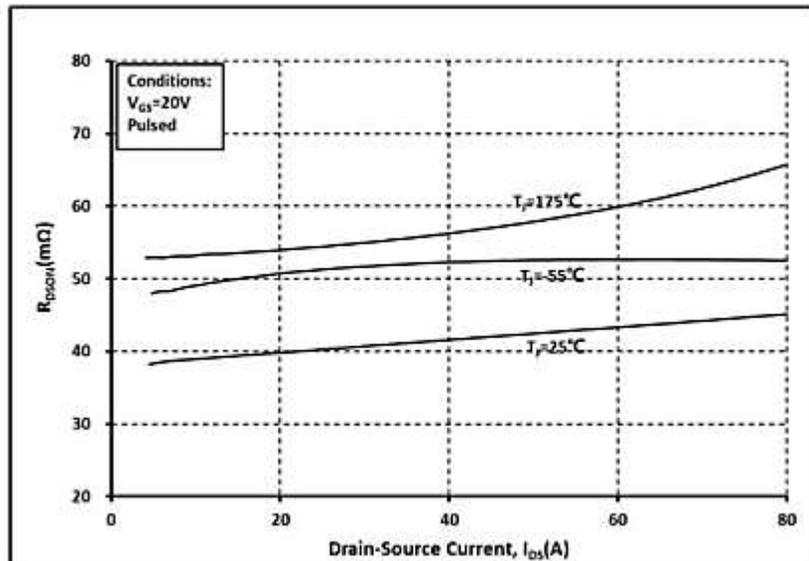


Figure 6. On-Resistance Vs. Drain-Source Current @ Various Temperature

TYPICAL PERFORMANCE (For Reference Only)

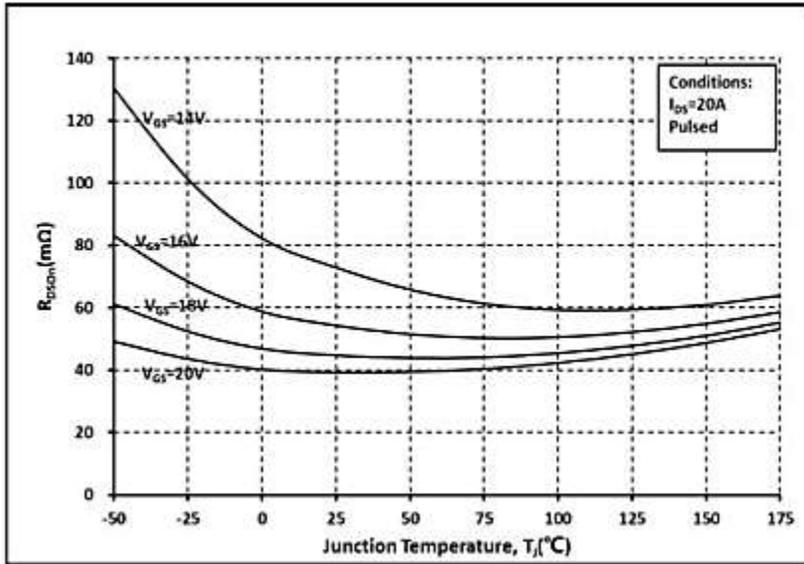


Figure 7. On-Resistance @ V_{GS} vs. Temperature

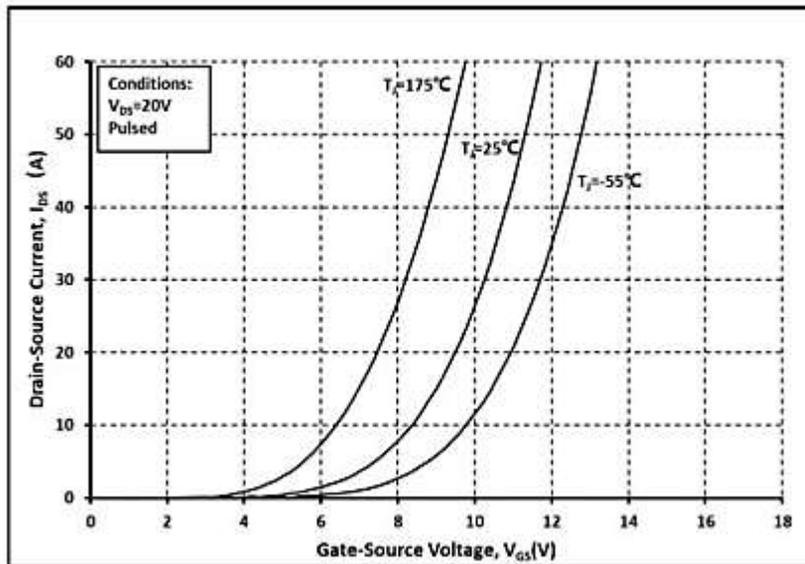


Figure 8. Transfer Characteristic For Various Junction Temperatures

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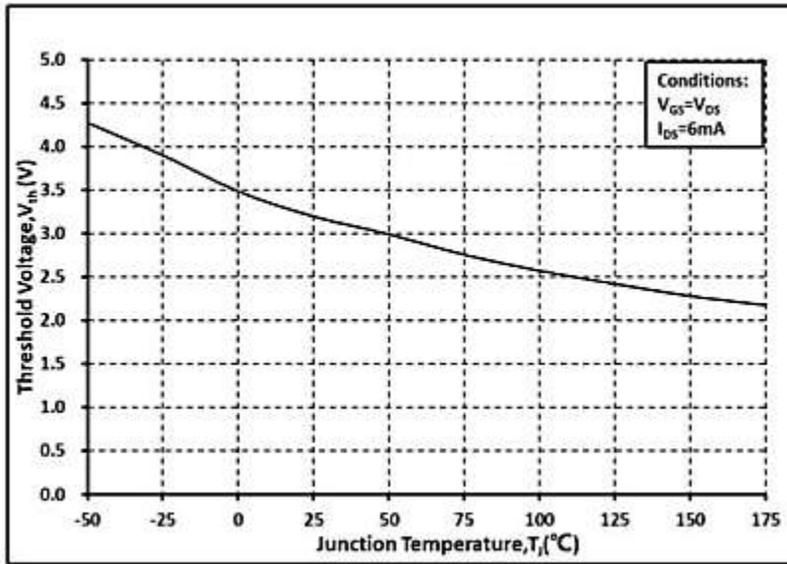


Figure 9. Threshold Voltage vs. Temperature

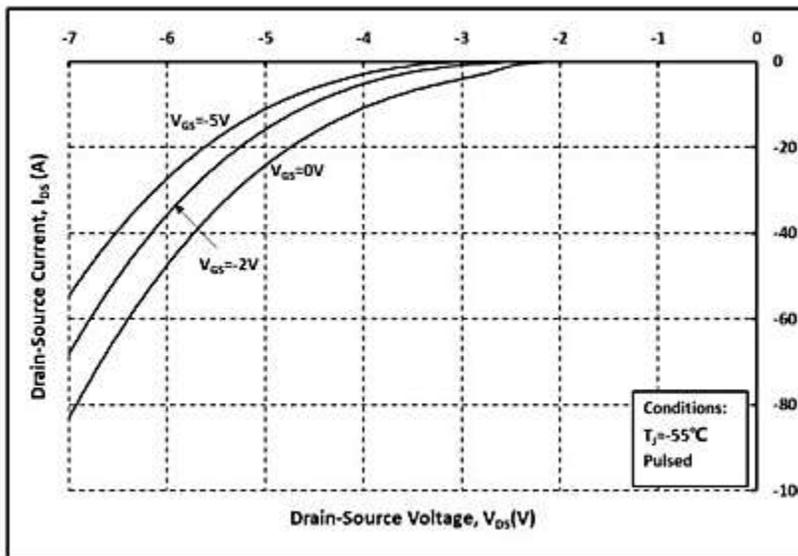


Figure 10. Body Diode Characteristics @ $T_j = -55^\circ\text{C}$

TYPICAL PERFORMANCE (For Reference Only)

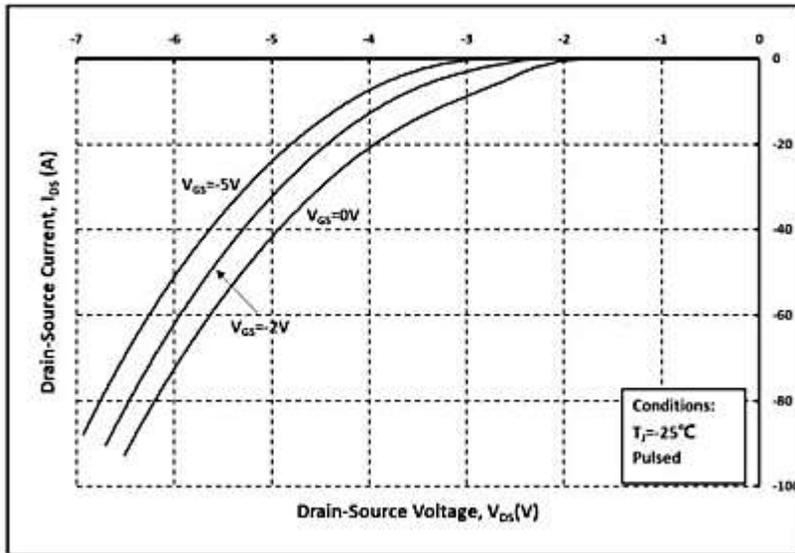


Figure 11. Body Diode Characteristics @ $T_J = 25^\circ\text{C}$

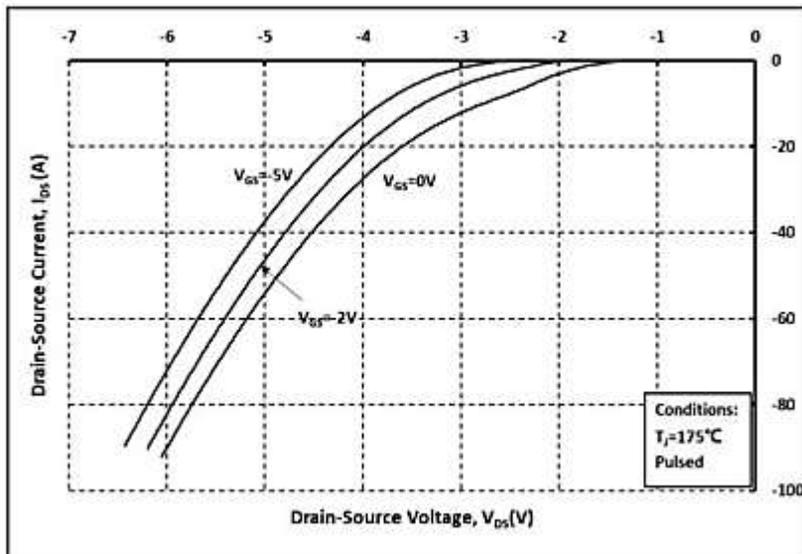


Figure 12. Body Diode Characteristics @ $T_J = 175^\circ\text{C}$

TYPICAL PERFORMANCE (For Reference Only)

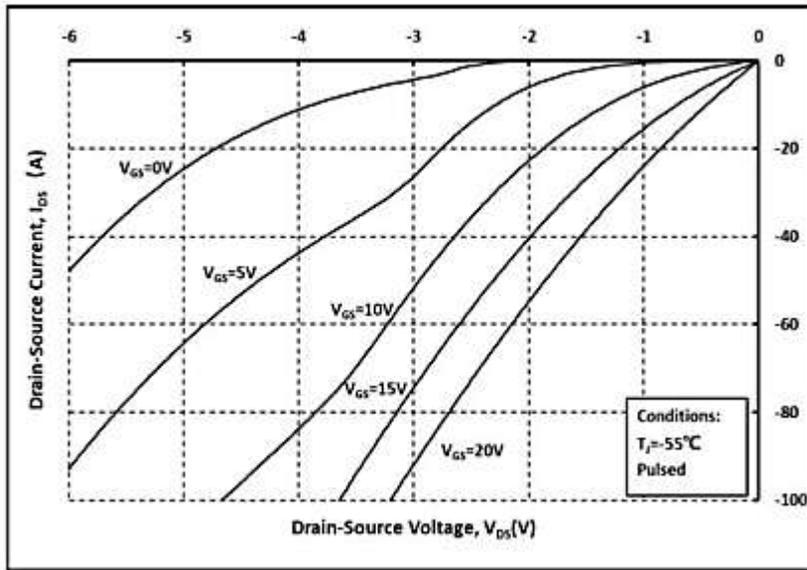


Figure 13. 3rd Quadrant Characteristics @ $T_j = -55^\circ\text{C}$

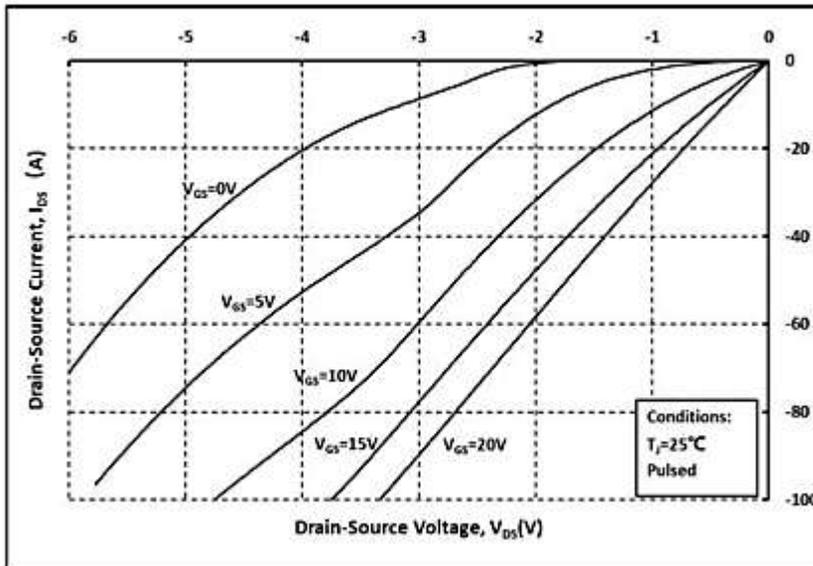


Figure 14. 3rd Quadrant Characteristics @ $T_j = 25^\circ\text{C}$

TYPICAL PERFORMANCE (For Reference Only)

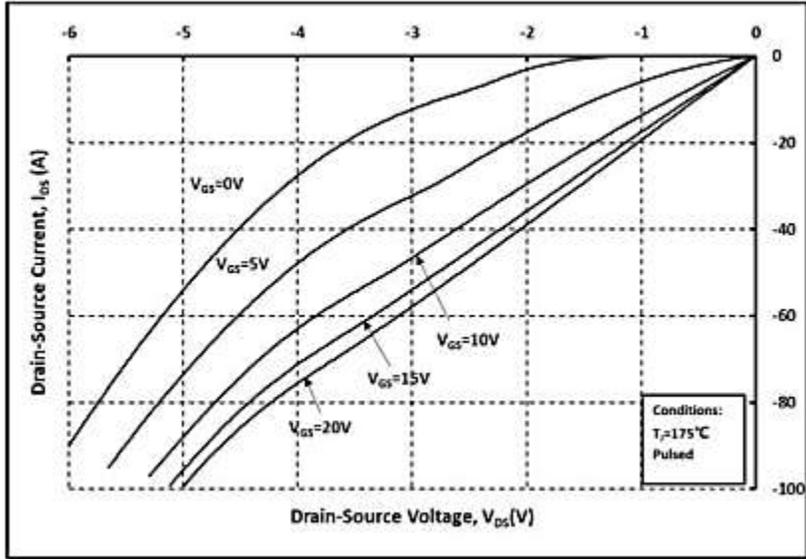


Figure 15. 3rd Quadrant Characteristics @ $T_J=175^\circ\text{C}$

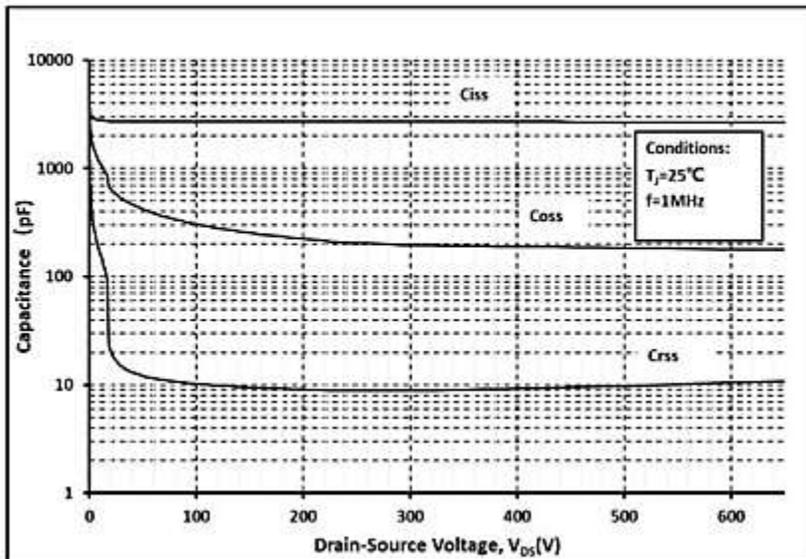


Figure 16. Capacitances vs. Drain-Source Voltage

TYPICAL PERFORMANCE (For Reference Only)

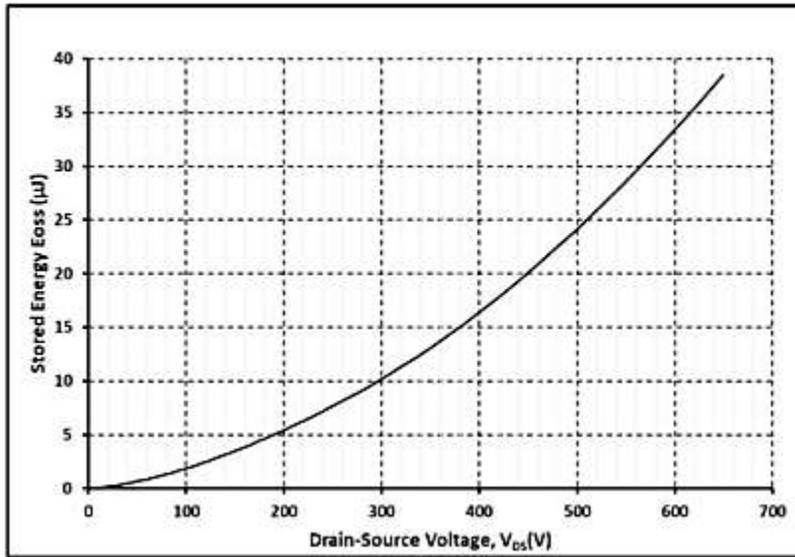


Figure 17. Output Capacitor Stored Energy

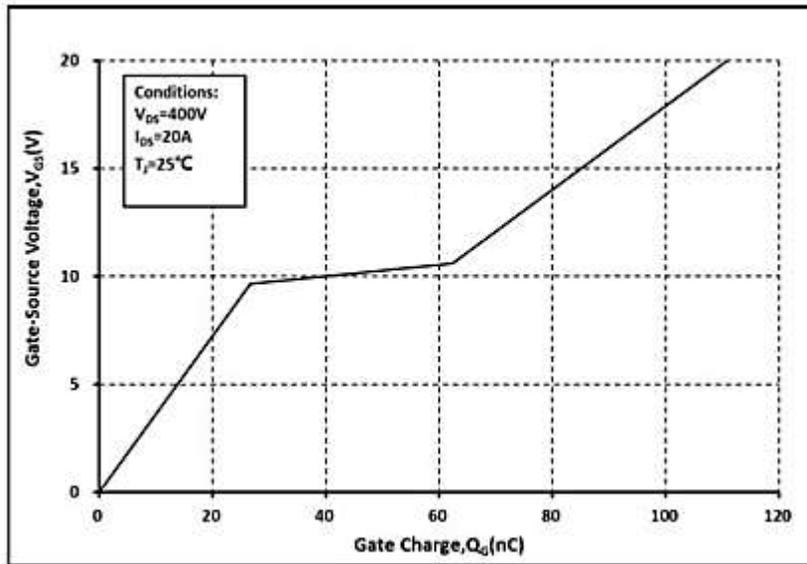


Figure 18. Gate Charge Characteristics

TYPICAL PERFORMANCE (For Reference Only)

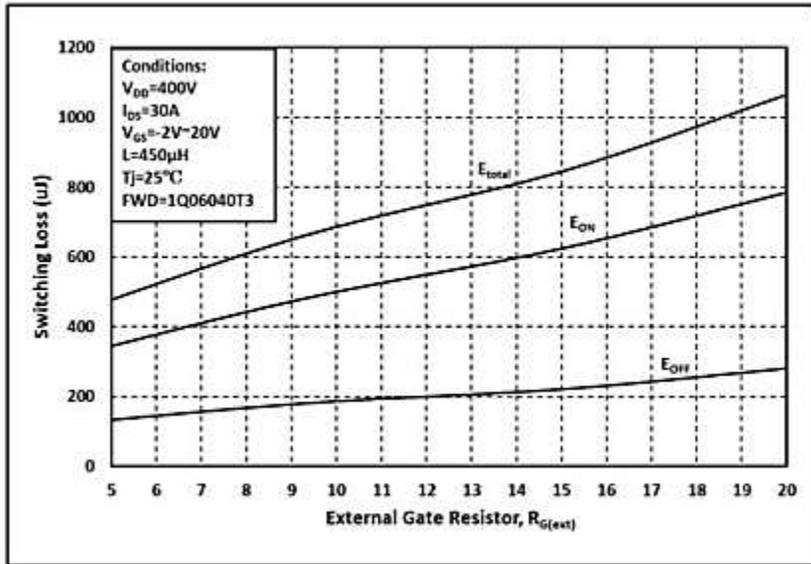


Figure 19. Switching Energy vs. External Gate Resistor

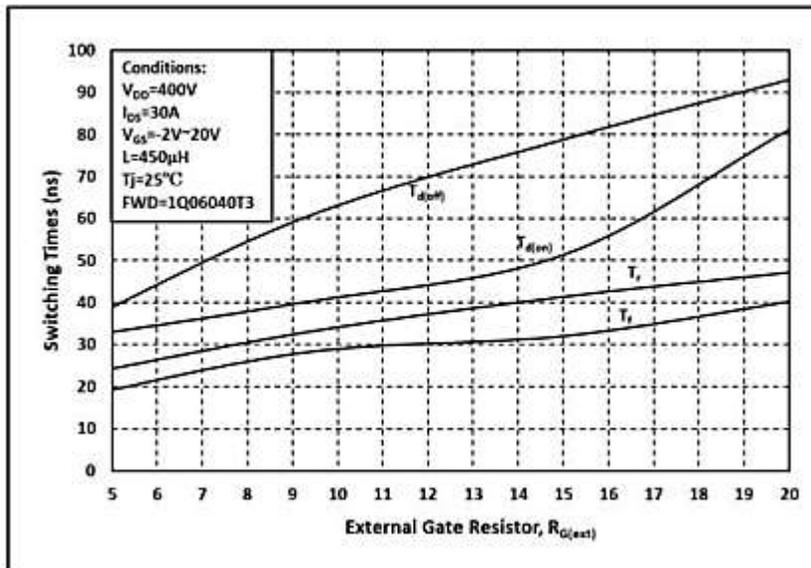


Figure 20. Switching Time vs. External Gate Resistor

TYPICAL PERFORMANCE (For Reference Only)

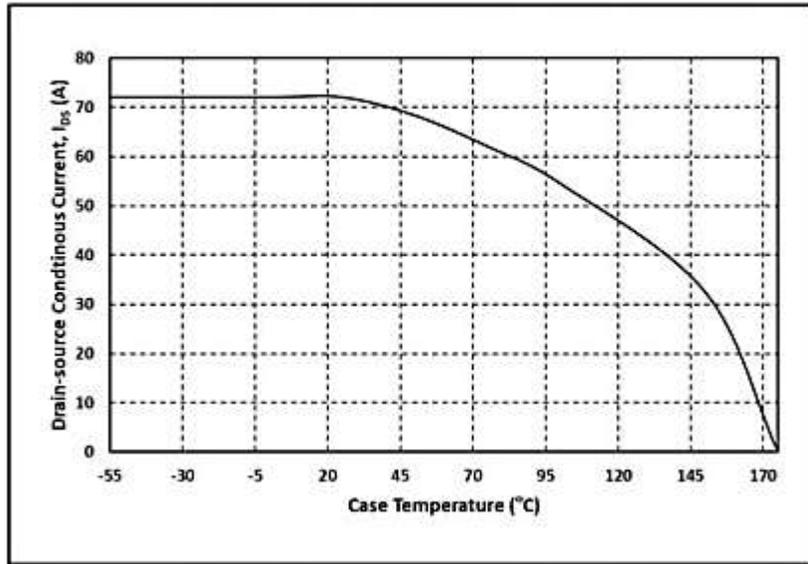


Figure 21. Continuous Drain Current Derating vs. Case Temperature

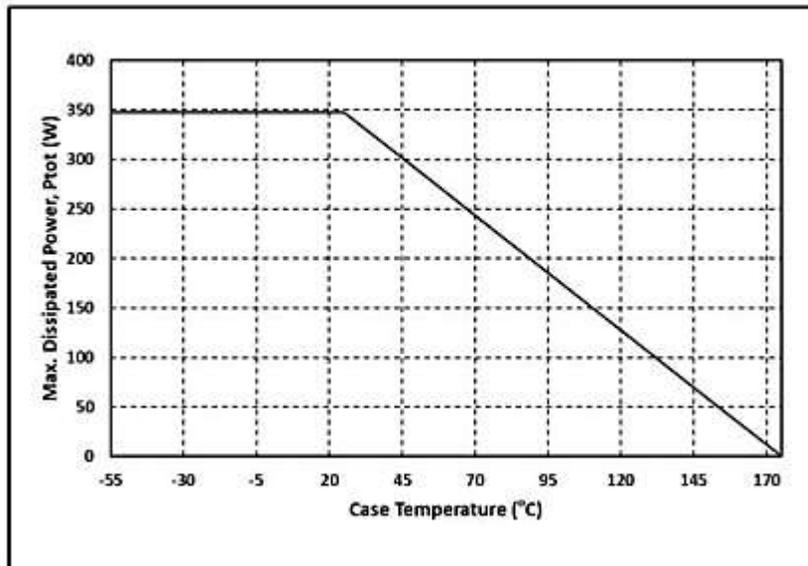


Figure 22. Maximum Power Dissipation Derating vs. Case Temperature

TYPICAL PERFORMANCE (For Reference Only)

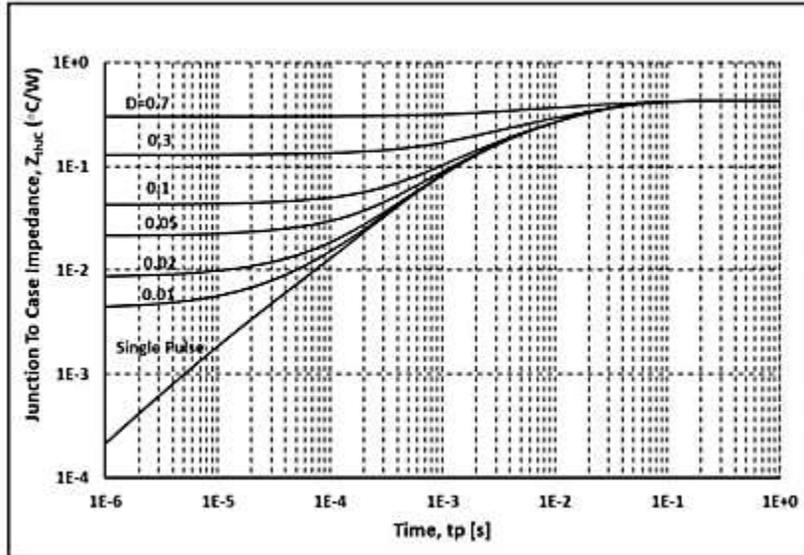


Figure 23. Transient Thermal Impedance (Junction to Case)

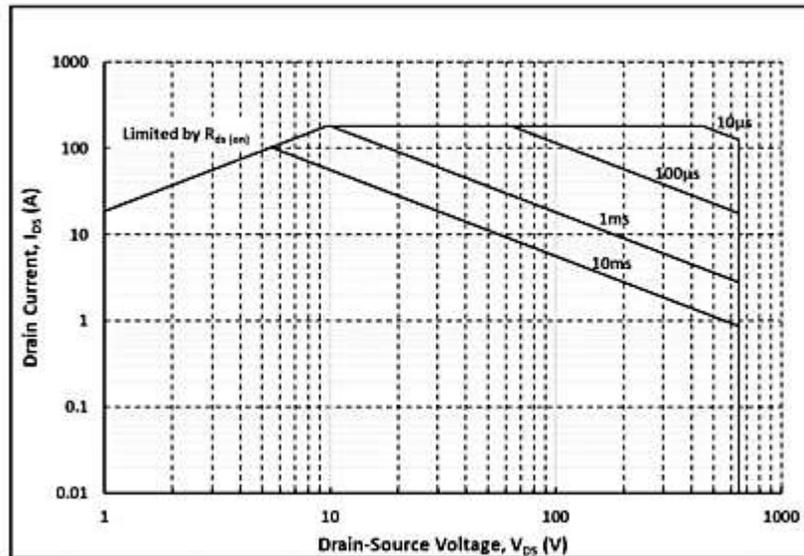


Figure 24. Safe Operating Area

IMPORTANT NOTES AND DISCLAIMER

1. **ROHS COMPLIANCE:** The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU RoHS Directive (EU) 2015/863 EC (RoHS3). RoHS Test Report for this product can be obtained at Download Center.
2. **REACH COMPLIANCE:** REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, REACH Test Report for this product can be obtained at Download Center.
3. All Product parametric performance is indicated in the Electrical Characteristics for the listed herein test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
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8. *NextGen* requires that customers first obtain an RMA (Returned Merchandise Authorization) number prior to returning any products. Returns must be made within 30 days of the date of invoice, be in the original packaging, unused and like-new condition. At the time of quoting or purchasing, a product may say that it is Non-Cancelable/ Non-Returnable (NCNR). These products are not returnable and not refundable.