

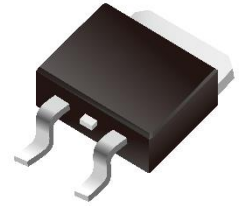
SPECIFICATION SHEET NO.	S0212- MDD15N10D000SD	
ORIGINAL MFG/PART NO.	MDD Diodes/MDD15N10D	
NEXTGEN PART CODE	MDD15N10D000SD	Indicate This Code For RFQ/Order
DATE	Feb. 12, 2025	
REVISION	A2	Updated With Most Recent Data
DESCRIPTION AND MAIN PARAMETRICS	100V N-Channel Enhancement Mode MOSFET, MDD15 Series, 3 pins, MDD15N10D Type, Case TO-252 Drain-Source Voltage (V_{BRDSS}):100V Continuous Drain Current (I_D): 15A Max. Power Dissipation (P_d): 28W Max. Junction Temperature (T_J): +150° C Package in Tape/reel, 2500pcs/Reel REACH/RoHS III/Compliant and Halogen Free (HF)	
CUSTOMER		
CUSTOMER PART NUMBER		
CROSS REF. PART NUMBER		
MEMO		

VENDOR APPROVE
Issued/Checked/Approved   
Effective Date: Feb. 12, 2025

CUSTOMER APPROVE
Date:

MAIN FEATURE

- Trench Power MV MOSFET Technology
- Excellent Package For Heat Dissipation
- High Density Cell Design For Low RDS(ON)
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- REACH/RoHS/RoHS III/Compliant
- Halogen Free



*Image shown is a representation only.
Exact specifications should be obtained
from the product dimension.*

APPLICATION

- DC-DC Converters
- Power Management Functions

ELECTRICAL CHARACTERISTICS

- See Page 6 ~Page 8.
- All Parameters are Subject To NextGen Components' Final Confirmation



HOW TO ORDER

- Please Follow Up Part Code Guide And Indicate NextGen Part Code MDD15N10D000SD For RFQ and Order.

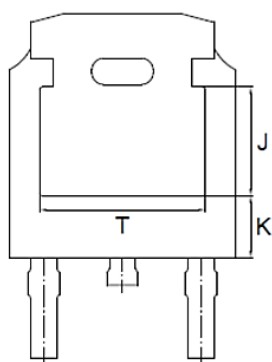
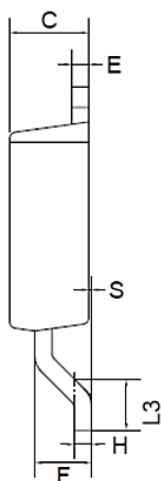
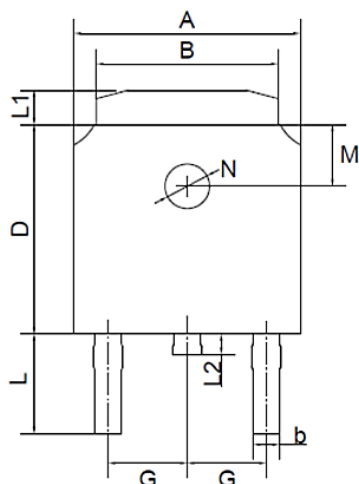
PART CODE GUIDE

RFQ

[Request For Quotation](#)

CODE	NAME	KEY SPECIFICATION OPTION
MDD15	Product Series Code	MOSFET, Continuous Drain Current (Id): 15A Max. MDD15 series, 3 pins
N	Enhancement Mode code	N: N-Channel Enhancement Mode
10	Drain-Source Voltage Code	10: 100V
D	Case Code	D: Case TO-252
000SD	Internal Control Code	Letter or Digits (A~Z, a~z or 1~9)
XX	Special/Custom Parameters Code	Letter or Digits (A~Z, a~z or 1~9) for Special Parametric; Blank: N/A

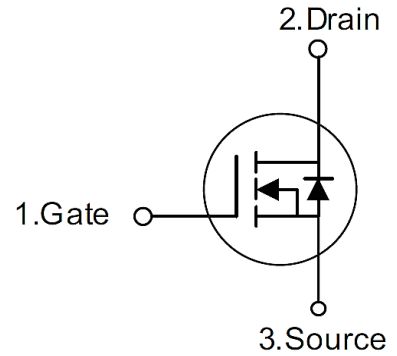
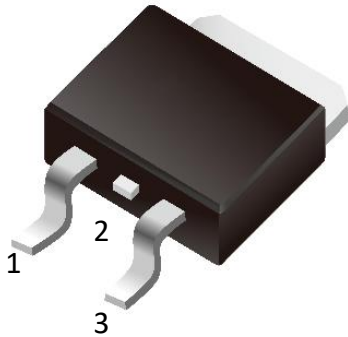
DIMENSION- Unit: mm, Case TO-252 Outline



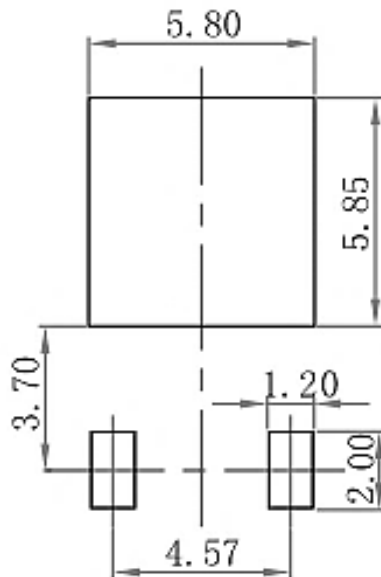
SYMBOL	VALUE (MM)		
	MIN.	TYP.	MAX.
A	6.30	-	6.70
B	5.10	-	5.50
b	0.30	-	0.80
C	2.10	-	2.50
D	5.90	-	6.30
E	0.40	-	0.60
F	1.30	-	1.80
G	2.29 Typical		
H	0.45	-	0.55
L	2.70	-	3.10
L1	0.80	-	1.20
L2	0.60	-	1.00
L3	1.40	-	1.75
S	0.0	-	0.10
M	1.80 Typical		
N	1.30 ref.		
J	3.16 ref.		
K	1.80 ref.		
T	4.83 ref.		

EQUIVALENT CIRCUIT

1. Gate (G) 2. Drain (D) 3. Source (S)



RECOMMEND PAD LAYOUT - - For Reference, Unit: Inch/mm, $\pm 0.05\text{mm}$



100V N-CHANNEL ENHANCEMENT MODE MOSFET

V (BR)DSS	Id Max	RDS (on), Max	Q g,typ
100V	15A	110mΩ @ VGS=10V 120mΩ @ VGS=4.5V	26 nC

MAX. RATINGS @Ta=25 °C (Unless Otherwise Specified)

PARAMETER	SYMBOLS	VALUE	UNIT
Drain-Source Voltage	V DS	100	V
Gate-Source Voltage	V GS	±20	V
Continuous Drain Current	I D	15	A
Pulsed Drain Current (Note 1)	I DM	60	A
Avalanche Energy Single Pulsed (Note 2)	E AS	9	mJ
Power Dissipation	P D	28	W
Thermal Resistance Junction-to-Case (Note 3)	R θJC	4.4	°C/W
Junction Temperature	T J	150	°C
Storage Temperature Range	T STG	-55 ~ +150	°C

Note:

- 1) Pulse Test: Pulse Width≤300us, Duty cycle ≤2%.
- 2) Tj=25°C, VDD=50V, VG=10V, L=0.5mH, IAS=6A
- 3) RθJA is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. RθJC is guaranteed by design, while RθJA is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

ELECTRICAL CHARACTERISTICS @Ta=25 °C (Unless Otherwise Specified)

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			Min.	Typ.	Max.	
Drain-Source Breakdown Voltage	V (BR) DSS	VGS=0V, ID=250μA	100	-	-	V
Drain-Source Leakage Current	IDSS	VDS=100V, VGS=0V	-	-	1	uA
Gates-Source Leakage Crurent	IGSS	VGS=±20V, VDS=0V	-	-	± 100	nA
Gate Threshold Voltage	VGS(TH)	VDS=VGS, ID=250μA	1.1	1.8	2.5	V
Drain-Source On-State Resistance	RDS(ON)	VGS=10V, ID=10A	-	95	110	mΩ
		VGS=4.5V, ID=8A	-	100	120	mΩ

DYNAMIC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			Min.	Typ.	Max.	
Input Capacitance	Ciss	VDS=50V, VGS=0V f=1MHz		1070	-	pF
Output Capacitance	Coss		-	33	-	pF
Reverse Transfer Capacitance	Crss		-	30	-	pF
Total Gate Charge	Qg	VDS=50V, VGS=10V, ID=10A (Note1, 2)	-	26	-	nC
Gate Source Charge	Qgs		-	5.4	-	nC
Gate Drain Charge	Qgd		-	5.8	-	nC

SWITCHING CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			Min.	Typ.	Max.	
Turn on Delay Time	$t_{d(on)}$	$V_{DS}=50V$, $V_{GS}=10V$, $R_G=3\Omega$ (Note1,2)	-	7	-	ns
Turn on Rise Time	t_r		-	24	-	ns
Turn Off Delay Time	$t_d(off)$		-	24	-	ns
Turn Off Fall Time	t_f		-	31	-	ns

SOURCE DRAIN DIODE CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	VALUE			UNIT
			Min.	Typ.	Max.	
Source drain current(Body Diode)	I_{SD}		-	-	15	A
Pulsed Current	I_{SM}		-	-	60	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=15A$, $V_{GS}=0V$	-	0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F=10A$, $dI/dt=100A/\mu s$	-	40	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	30.1	-	nC

Note:

- 1) Pulse test ; Pulse width 300us, duty cycle 2%
- 2) Essentially independent of operating temperature

TYPICAL PERFORMANCE - For Reference Only

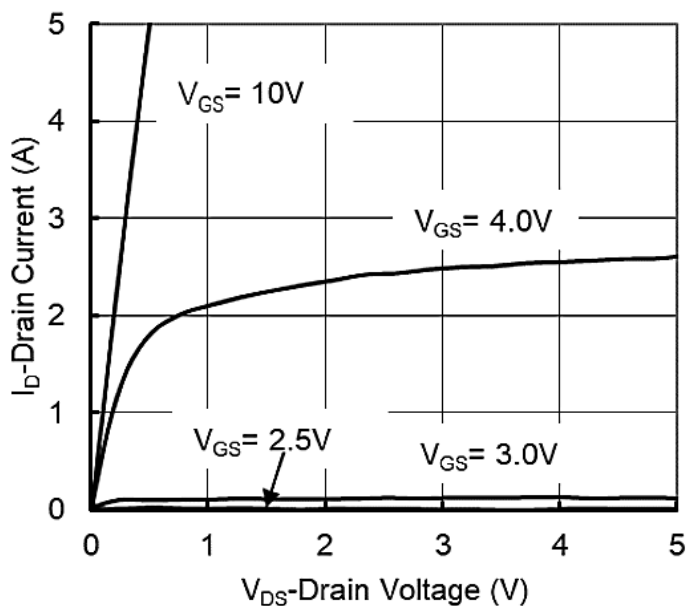


Figure 1. Output Characteristics

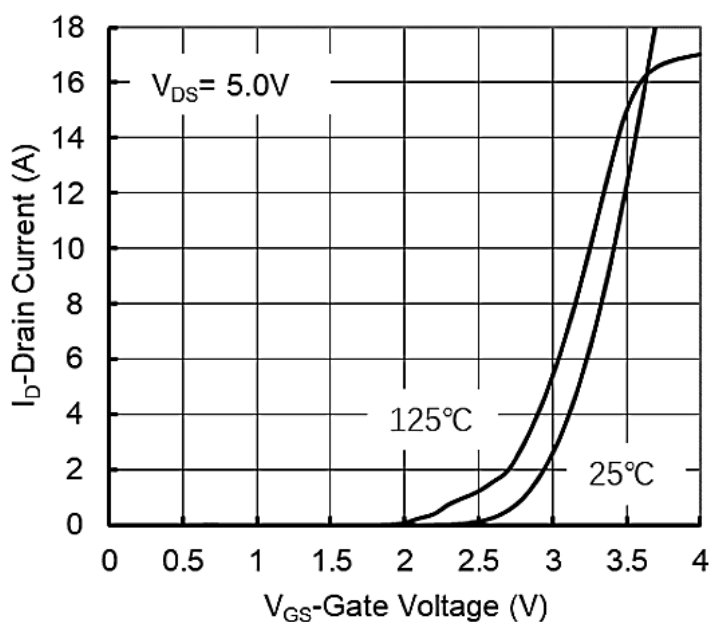


Figure 2. Transfer Characteristics

TYPICAL PERFORMANCE - For Reference Only

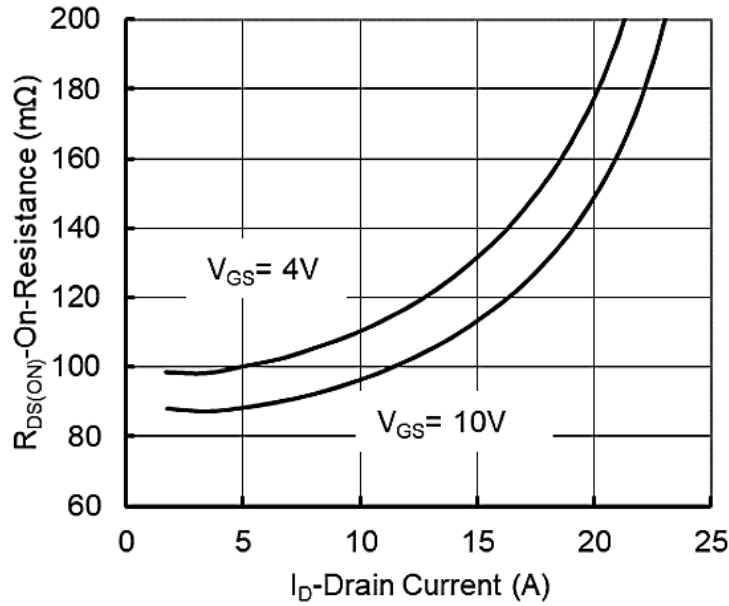


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

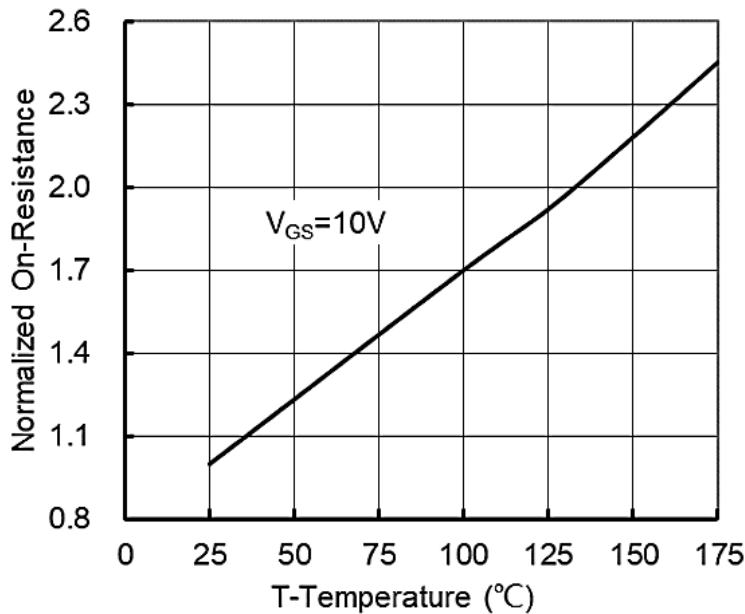


Figure 4. On-Resistance vs. Junction Temperature

TYPICAL PERFORMANCE - For Reference Only

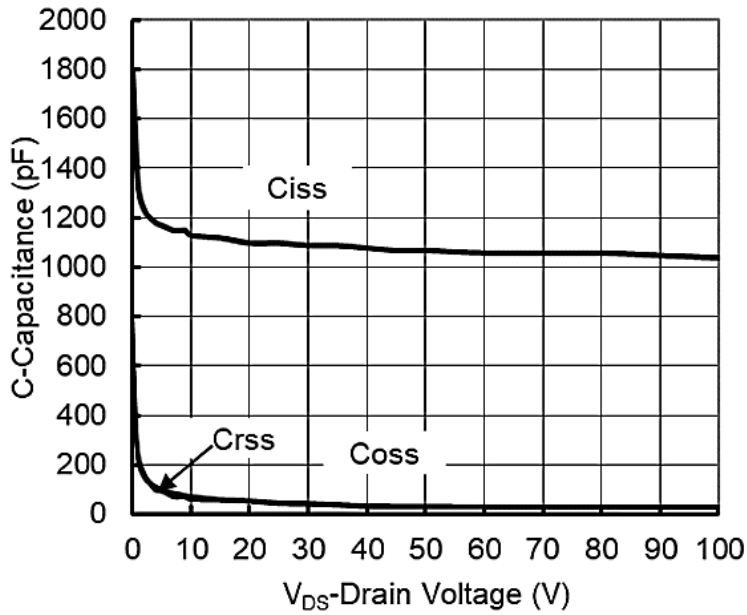


Figure 5. Capacitance Characteristics

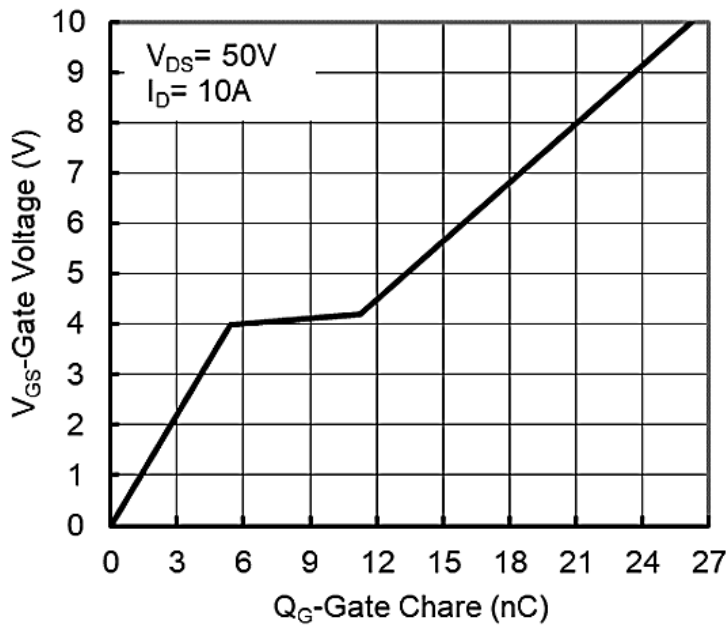


Figure 6. Gate Charge

TYPICAL PERFORMANCE - For Reference Only

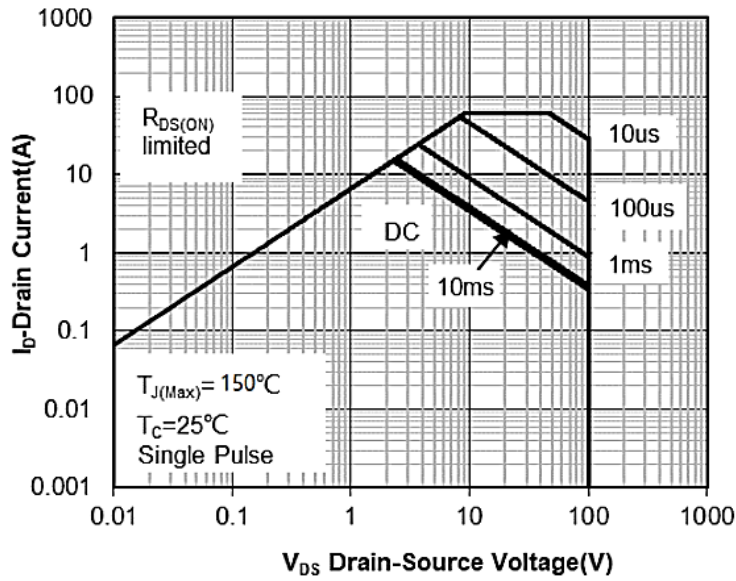


Figure 7. Safe Operation Area

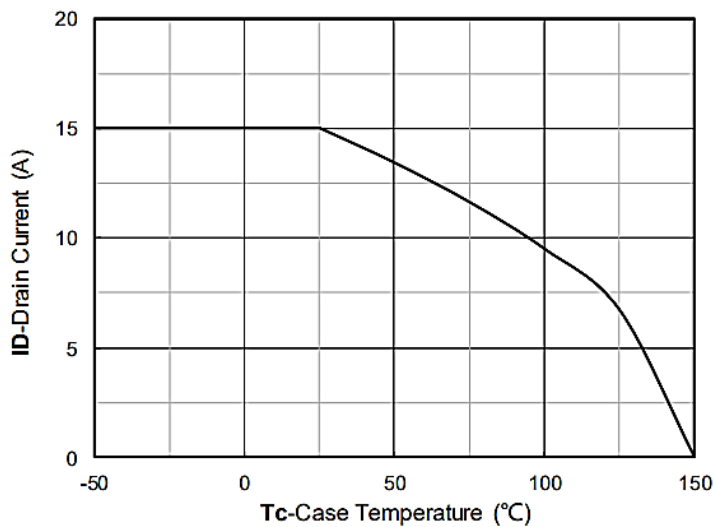


Figure 8. Maximum Continuous Drain Current vs Case Temperature

TYPICAL PERFORMANCE - For Reference Only

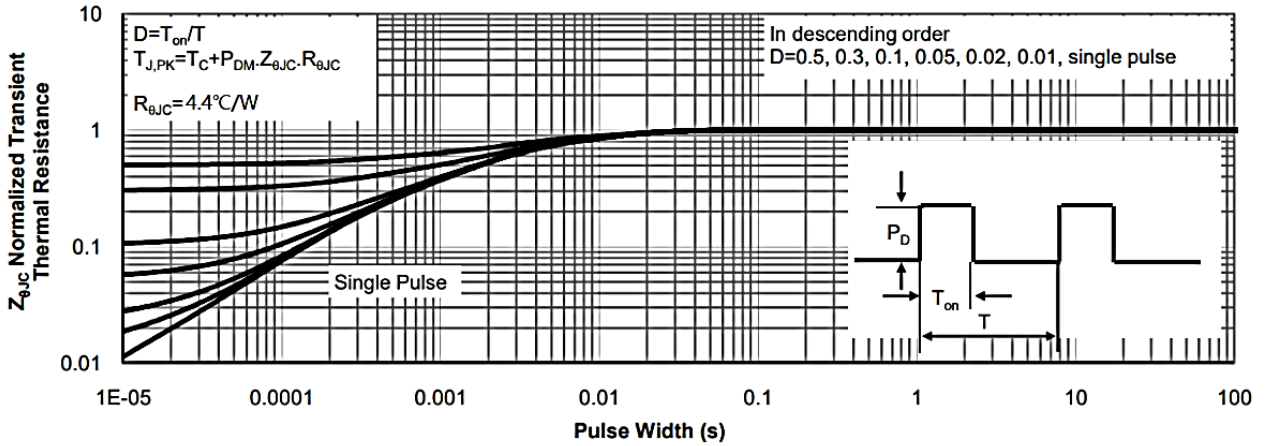
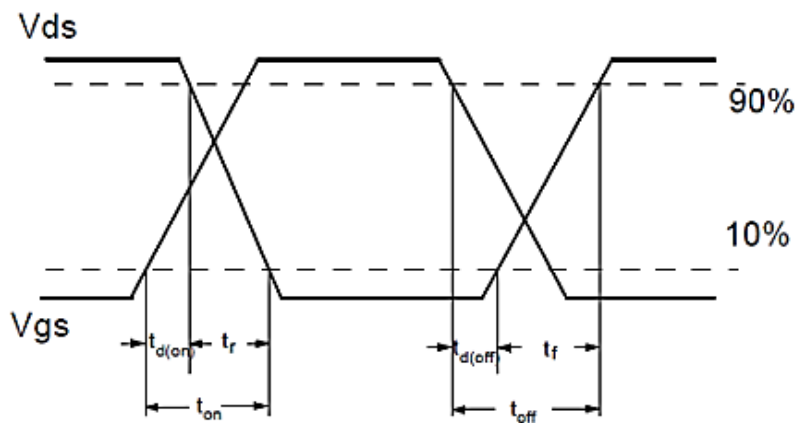
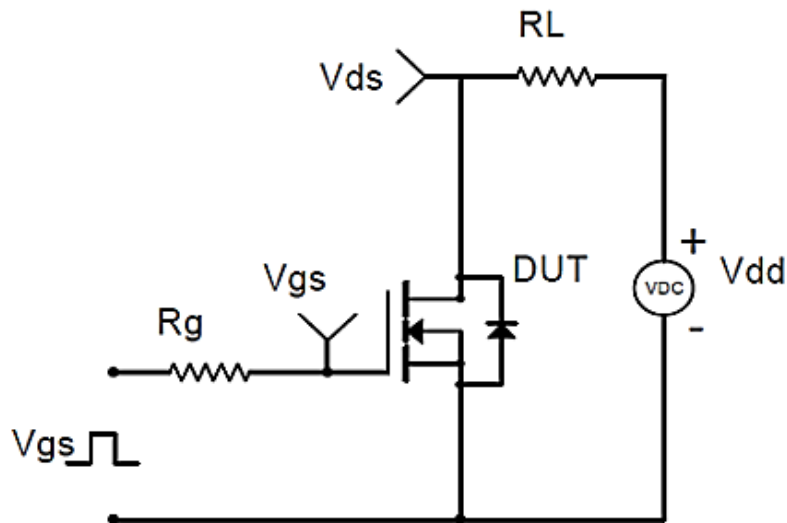
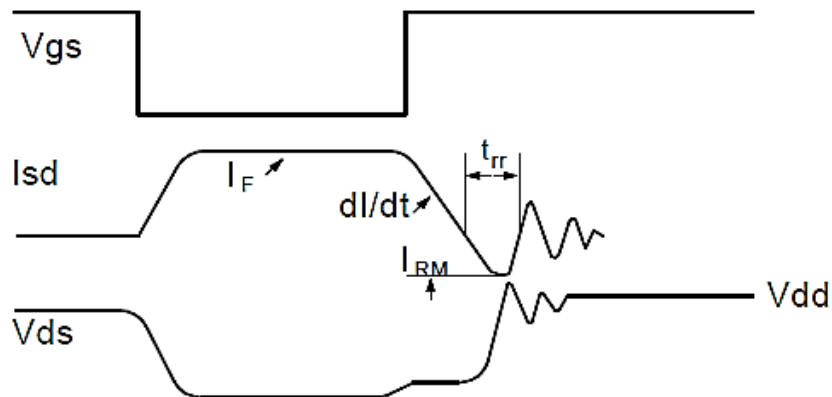
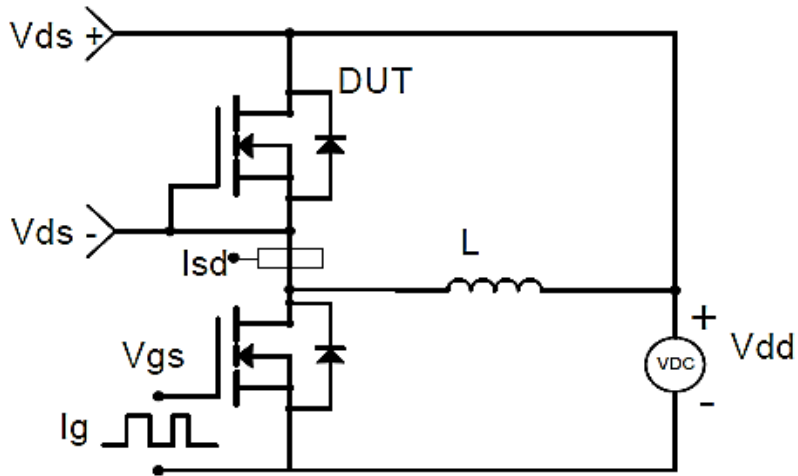


Figure 9. Normalized Maximum Transient Thermal Impedance

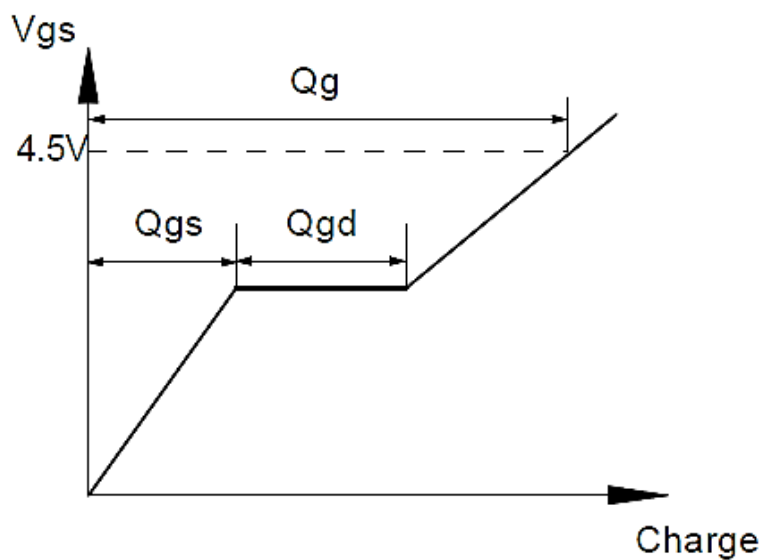
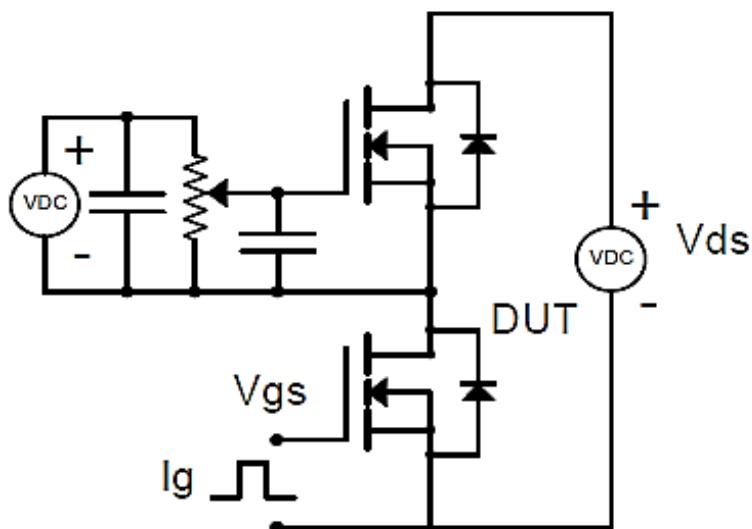
RESISTIVE SWITCHING TEST CIRCUIT & WAVEFORMS



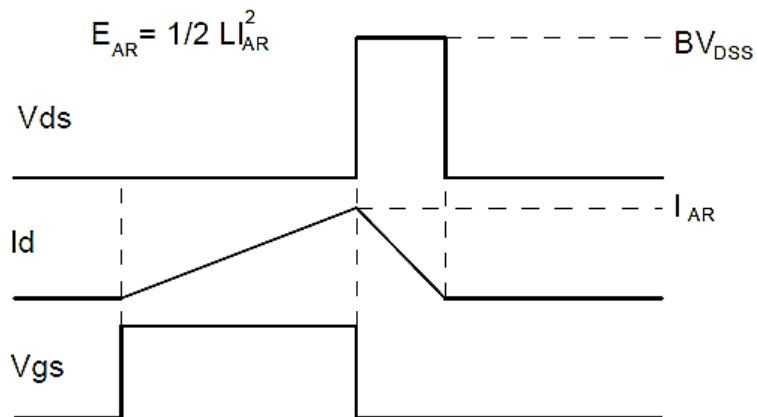
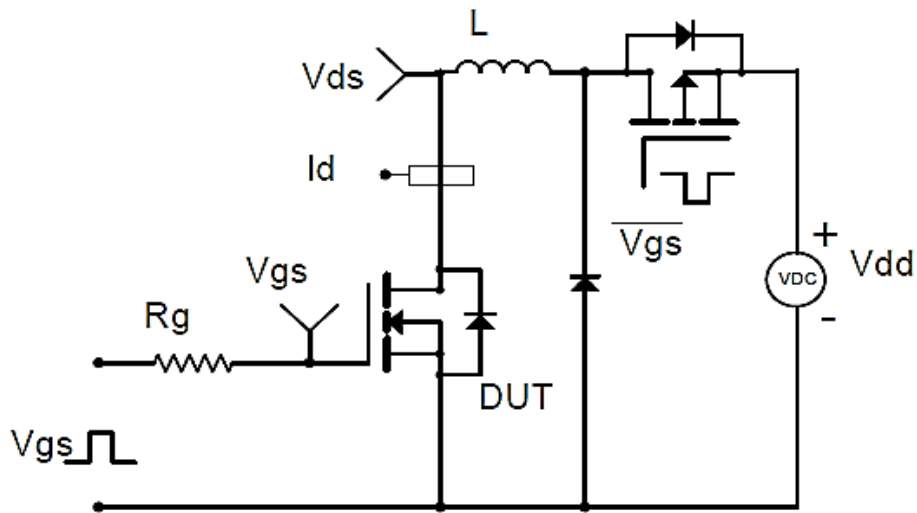
DIODE RECOVERY TEST CIRCUIT & WAVEFORMS



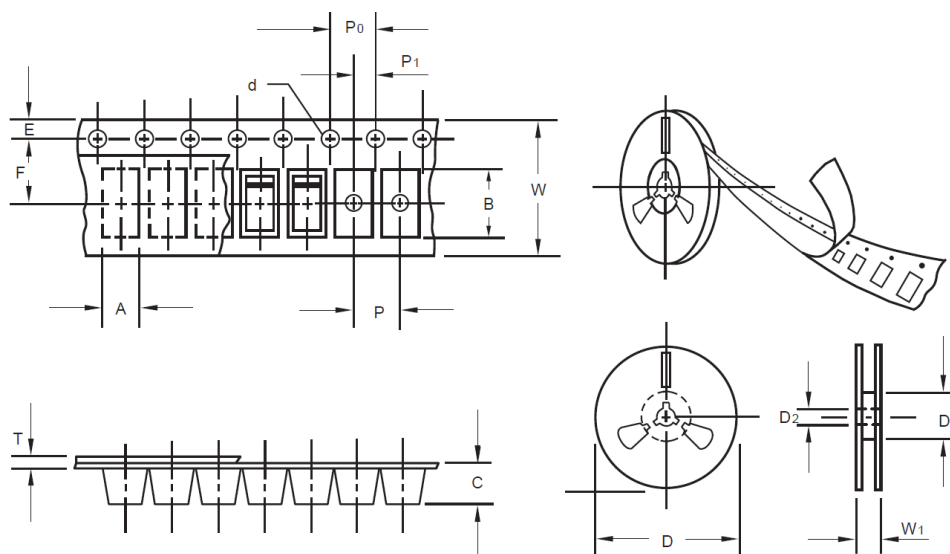
GATE CHARGE TEST CIRCUIT & WAVEFORM



UNCLAMPED INDUCTIVE SWITCHING (UIS) TEST CIRCUIT & WAVEFORMS



TAPE/REEL - Unit: mm, All Devices are packed in accordance with EIA standard RS-481-A and specifications



ITEM	SYMBOL	TOLERANCE	TO-252
Carrier width	A	0.1	6.15
Carrier Length	B	0.1	8.41
Carrier Depth	C	0.1	2.42
Sprocket hole	d	0.05	1.50
13"Reel outside diameter	D	2.0	330.00
13"Reel inner diameter	D1	Min.	50.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	7.50
Punch hole pitch	P	0.1	8.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.25
Tape width	W	0.3	16.00
Reel width	W1	1.0	16.50
SPQ Per Reel	2500pcs		

IMPORTANT NOTES AND DISCLAIMER

1. **ROHS COMPLIANCE:** The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU RoHS Directive (EU) 2015/863 EC (RoHS3). RoHS Test Report for this product can be obtained at Download Center.
2. **REACH COMPLIANCE:** REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, REACH Test Report for this product can be obtained at Download Center.
3. All Product parametric performance is indicated in the Electrical Characteristics for the listed herein test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
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