

## **SPECIFICATION SHEET**

SPECIFICATION SHEET NO.	N0511- MTP29152R2ML01
DATE	May 11, 2021
REVISION	A0
DESCRIPITION	Thru Hole High Current Power Inductor, MTP series
	Inductance (L) 2.2µH, Tolerance 20%
	Operating Temp. Range -55°C ~+125°C,
	Package in Tray, 40pcs/Tray
	RoHS/RoHS III compliant
CUSTOMER	
CUSTOMER PART NUMBER	
CROSS REF. PART NUMBER	
ORIGINAL PART NUMBER	MTP2915-2R2M
PART CODE	MTP29152R2ML01

VENDOR APPROVE			
Issued/Checked/Approved	Component Mandy Xu ZoV Xu	Component Compon	Low Parent
DATE: May 11, 2021			
CUSTOMER APPROVE			

DATE:



### THRU HOLE HIGH CURRENT POWER INDUCTORS MTP SERIES

#### **MAIN FEATURE**

- Temperature rise current and saturation current is less influenced by environment.
- High inductance, high current, low magnetic loss low ESR and small parasitic capacitance.
- Flat wire winding, achieve a low D.C. Resistance.
- Assemblage design, sturdy structure.

#### APPLICATION

- Low Profile and High current power Supplies
- Battery power device
- DC/DC converters in distributed power system
- DC/DC converters for Field programmable gate array

<u>RFQ</u>
Request For Quotation

#### PART CODE GUIDE

MTP2915	2R2M	L	01
1	2	3	4

1) MTP2915: Series code for Thru Hole High Current Power Inductor, MTP series, Dimension L28.0\*W19.0\*H15.0mm

2) **2R2M**: Specification code: Inductance (L)  $2.2\mu$ H, Tolerance 20%

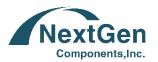
3) L: Package Code, Packed in Bulk, 40pcs/Tray.

4) 01: Internal Control Code or special code required by customer-letter or digits

#### **DIMENSION (Unit: mm)**

Tope View	Side View	Bottom View	Image for Ref.
A:28MAX D:27MAX B:19=0.2 E:4=0.2 F:14.5=0.5	C: 15 ± 0. 5 <u>G: 4. 8 ± 0. 2</u> <u>H: 6. 0 ± 0. 5</u>		

Item	Dimension
item	Dimension
Α	28.0 Max.
В	19.0±0.2
С	15.0±0.5
D	27.0 Max.
E	4.0±0.5
F	14.0±0.5
G	4.8±0.2
н	6.0±0.5



### THRU HOLE HIGH CURRENT POWER INDUCTORS MTP SERIES

### **ELECTRONICAL CHARACTERISTICS**

Parameter		Specification	Condition	Test Instruments
L (µH)	S-F	2.2+/-20%	100KHz/0.25V	MICROTEST 6377
DCR (mΩ)	S-F	3.0 Max.	25 ℃	TH2512A
I sat (A) S-F	S-F	60.0 A Typical LOA*80%	100KHz/0.25V	MICROTEST 6377+6220
I rms (A) S-F	S-F	30.0A Typical △T≤40°C	100KHz/0.25V	MICROTEST 6377+6220

#### **ELECTRICAL SCHEMATIC CIRCUIT**



### **CHARACTERISTICS**

1) All test data is based on 25 °C ambient.

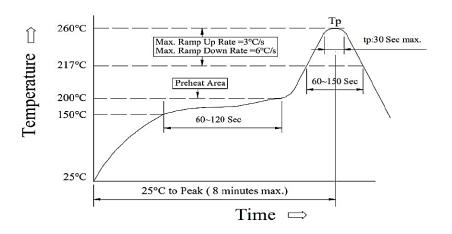
2) DC current (A) that will cause an approximate  $\Delta$ T40 °C

3) DC current (A) that will cause L0 to drop approximately 20% Typical.

4) Operating temperature range : -55°C~+125 °C

5) The part temperature(ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component. PWB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the den application

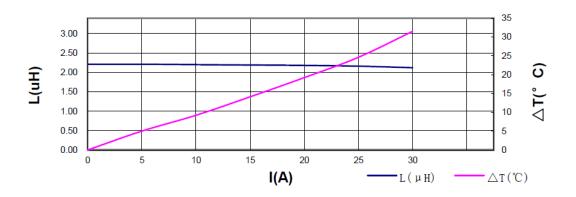
#### **REFLOW SOLDERING PROFILE**



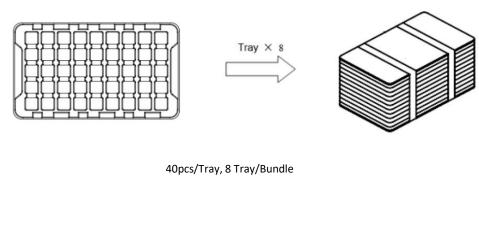


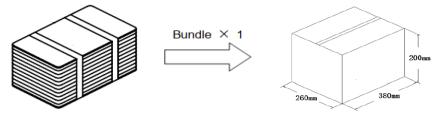
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#### **CURRENT CHARACTERISTICS**



#### PACKAGE DIMENSION (Unit: mm)





I Bundle/Carton, Total: 320pcs



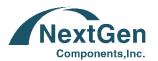
### THRU HOLE HIGH CURRENT POWER INDUCTORS MTP SERIES

#### **ENVIRONMENTAL TEST**

Items	Required Characteristics	Test Method/Condition
High Temp. Storage Test Ref. Document: MIL-STD-202G Method 108A	1.No case deformation or change in appearance2. $\Delta L/L \le 10\%$ or 15%3. $\Delta Q/Q \le 30\%$ 4. $\Delta DCR/DCR \le 10\%$	Temperature: N+/-2 °C Time: 96+/-2 hours Test not less than 1 hour and no more than 2 h at 25 °C (See Table 1- Fig. 1)
Low Temp. Storage Test Ref. Document: IEC 68-2-1A 6.1 & 6.2	1.No case deformation or change in appearance2. $\Delta L/L \le 10\%$ or 15%3. $\Delta Q/Q \le 30\%$ 4. $\Delta DCR \le 10\%$	Temperature: M+/-2 °C Time: 96+/-2 hours Test not less than 1 h and no more than 2 hours at 25 °C (See Table 1- Fig. 2)
Humidity test Ref. Document: MIL-STD-202G Method 103A	1. No case deformation or change in appearance 2. $\Delta L/L \le 10\%$ or 15% 3. $\Delta Q/Q \le 30\%$ 4. $\Delta DCR \le 10\%$	Temperature: 40+/-2 °C Humidity: 93+/-3% RH Time: 96+/-2 Hours Test not less than 1 hour and no more than 2 hours at 25 °C
Thermal shock Test Ref. Document: MIL-STD-202G Method 107G	<ol> <li>No case deformation or change in appearance</li> <li>Δ L/L ≤ 10% or 15%</li> <li>Δ Q/Q ≤ 30%</li> <li>Δ DCR ≤ 10%</li> <li>For T: Weight ≤28g; 15 Min. M: low temp.</li> <li>28g≤weight≤136g: 30 min N high temperature</li> </ol>	First M for T time, last N °C time as 1 cycle. GO through 20 cycles. (See Table 1- Fig. 3)

#### **PHYSICAL CHARACTERISTICS TEST**

Items	Required Characteristics	Test Method/Condition
Solderability Test Ref. Documents MIL-STD-202G Method 208H IPC-J-STD-002C	Terminate area must have 95% Min.	<ol> <li>Dip pads in flus then dip in solder at 260 +/-5 °C for 5 second</li> <li>Solder: Lead free</li> <li>Flux: rosin flux</li> </ol>
Heat endurance of Reflow Soldering Ref. Documents IPC-J-STD-020D	1.No case deformation or change in appearance2. $\Delta L/L \le 10\%$ or 15%3. $\Delta Q/Q \le 30\%$ 4. $\Delta DCR/DCR \le 10\%$	<ol> <li>Refer to the mentioned Reflow Curve and go through 3 times</li> <li>The peak temperature: 260 +/-5 °C</li> </ol>
Vibration Test Ref. Documents MIL-STD-202G Method 201A	1.No case deformation or change in appearance2. $\Delta L/L \le 10\%$ or 15%3. $\Delta Q/Q \le 30\%$ 4. $\Delta DCR/DCR \le 10\%$	Apply frequency 10-55Hz 1.5mm amplitude in each of perpendicular direction X, Y Z for 2 hours (total: 6 hours) (See Table 1-Fig. 4)
Drop Test Ref. Documents MIL-STD-202G Method 203C	1.No case deformation or change in appearance2. $\Delta L/L \le 10\%$ or 15%3. $\Delta Q/Q \le 30\%$ 4. $\Delta DCR/DCR \le 10\%$	Packaged & Drop down from 1m with 981m/s <sup>2</sup> (100G) attitude in 1 angle and 2 surfaces orientations
Terminal strength push Test Ref. Documents JIS C 5321:1997	Pulling test: Define A: sectional area of terminal 0.5mm <sup>2</sup> < A ≤ 1.2mm <sup>2</sup> ; Force≥20N time: 10 sec. 1.2mm <sup>2</sup> < A ; force≥20N time: 10 sec. Bending Test: Soldering the product on PCB, after the pulling test and bending test, terminal should not pull off.	Bend the testing PCB at middle point the deflection shall be 2mm (See Table 1-Fig. 5)
Resistance to Solvent Test Ref. Document IEC 68-2-45: 1993	No case deformation or change in appearance or obliteration of marking	To dip parts IPA solvent for 5+/-0.5 minute then drying them at room temperature for 5 minute, at last to brushing marking 10 times

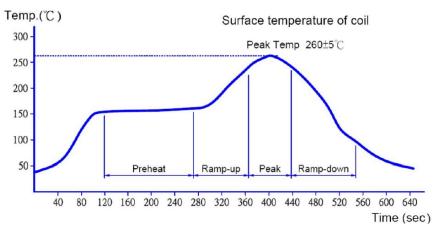


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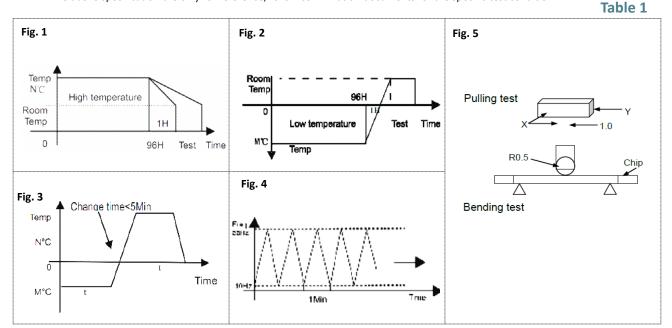
### **ELECTRICAL CHARACTERISTIC TEST**

Items	Required Characteristics	Test Method/Condition
Electronic Characteristic test of major products	Refer to catalogue of specific products	Refer to catalogue of specific products
Overload Test Ref. Document: JIS C5311-6.13	<ol> <li>During the test no smoke, no peculiar, smell, no fire</li> <li>The characteristic is normal after tested</li> </ol>	Apply twice rated current for 5 minutes (It's not application to some special design)
Voltage resistance test Ref. Document: MIL-STD-202G Method 301	<ol> <li>During the test no breakdown</li> <li>The characteristic is normal after tested</li> </ol>	<ol> <li>For parts with no coils</li> <li>DC 100V; Current: 1 mA, 1 minute</li> </ol>

#### **CURVE OF HEAT ENDURANCE OF REFLOW SOLDERING TEST**



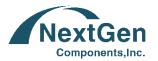
- 1. This peak temperature only applicable to some specific parts. The operating parameter may be different according to the pay type.
- 2. A test made under the conditions mentioned above and it is left 1 hour in the normal temperature.
- 3. The reflow condition is according to the machine used by own company.
- 4. The above specification are only for reference, follow confirmation documents for the specific test condition



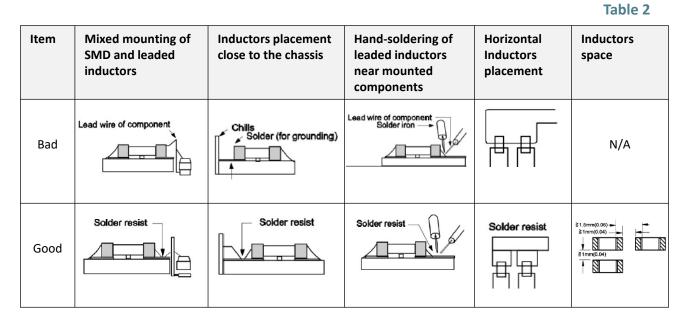


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Item	Application Notice
Storage Condition	<ul> <li>To maintain the solderability of terminal electrodes</li> <li>1. Temperature and humidity conditions: less than 30% and 60% RH.</li> <li>2. Recommended ceramic chip inductors should be used within 6 month after delivery.</li> <li>3. The package material should be kept where no chlorine or sulfur exists in the air.</li> </ul>
Handling	<ol> <li>The use of tweezers or vacuum pick ups is strongly recommended for individual components.</li> <li>Bulk handling should ensure that abrasion and mechanical shock are minimized.</li> <li>Chip multilayer ceramic inductors should be handled with care to avoid damage or contamination from perspiration and skin oils.</li> </ol>
Design of Land Pattern	<ul> <li>Component pads should be designed to achieve good solder filets and minimize component movement during reflow soldering. Pads designs are given below for the most common sizes of multilayer ceramic inductors for both wave and reflow soldering. The basis of these designs are: <ol> <li>Pad width equal to component width it is permissible to decease this to as low 85% of components width but it is not advisable to go below this.</li> <li>Pad overlap 0.5mm beneath component.</li> <li>Pad extension 0.5mm beyond components for reflow and 1.0mm for wave soldering.</li> <li>Example of good and band solder application.(see table 2)</li> <li>Components space: For wave soldering component, must be spaced sufficiently far apart to avoid bridging or shadowing (inability of solder to penetrate properly into small space) and ensure space must</li> </ol> </li> </ul>
Preheat	be allowed to enable rework should it be required. (see table 2) It is important to avoid possibility of thermal shock during soldering and carefully controlled preheat is therefore required. The rate of preheat should no exceed 4°C/sec. and a target figure 2°C/sec.is recommended. Although an 80°C to 120°C temperature differential is preferred, recent test result allow a temperature of 150°C Max. for components of 1.2*1.0mm size and below with a Max. thickness of 1.25mm. The users is cautioned that the risk thermal shock increases as chip size or temperature differential increases.
Solderability	Terminations to be well soldered after immersion in a Sn(96.5), Ag (3.5) tin/lead solder bath at 260+/-50°C for 5+/-1 seconds.
Selection of Flux	<ul> <li>Since flus may have a signification effect on the performance of components, it is necessary to verify following conditions prior to use.</li> <li>1. Flux used should be with less than or equal to 0.1 wt% (equivalent to Chlorine) of halogenated content, Flus having a strong acidity content should not be applied.</li> <li>2. When soldering component on the PCB, the amount of Flux applied should be controlled at optimum level</li> <li>3. When using water should be flux, special care should be taken to properly clean the PCB.</li> </ul>
Soldering	<ul> <li>Mildly activated rosin fluxes are preferred. The minimum amount of solder for give a good joint should be used excessive solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, ship and substrate, 3L terminations are suitable for all wave and reflow soldering system. If hand soldering can't be avoided, the preferred technique is the utilization of hot air soldering tool. (see table 3)</li> <li>1. Solder Reflow: Recommended temperature profile.</li> <li>2. Solder Wave: Wave soldering is perhaps the most rigorous of SMT soldering processes due to the steep rise in temperature seen by the circuit when immersed in the molten solder wave typically at 240°C. Wave soldering of ceramic chip inductors larger then 1.8*1.2mm size is discouraged due to the risk of thermal damage to the inductors Recommended temperature profile.</li> <li>3. Soldering Iron: <ul> <li>(1) Preheat circuit and inductor to 150°C.</li> <li>(2) Never contact the ceramic with the iron tip</li> <li>(3) Use a 20 watt soldering iron with tip diameter of 1.0mm.</li> <li>(4) 280 °C tip temperature Max.</li> <li>(5) 1.0mm tip temperature Max.</li> <li>(6) Limited soldering time is less than 3 seconds</li> </ul> </li> </ul>



### THRU HOLE HIGH CURRENT POWER INDUCTORS MTP SERIES



#### Table 3

